

FIG. 1  
Prior Art

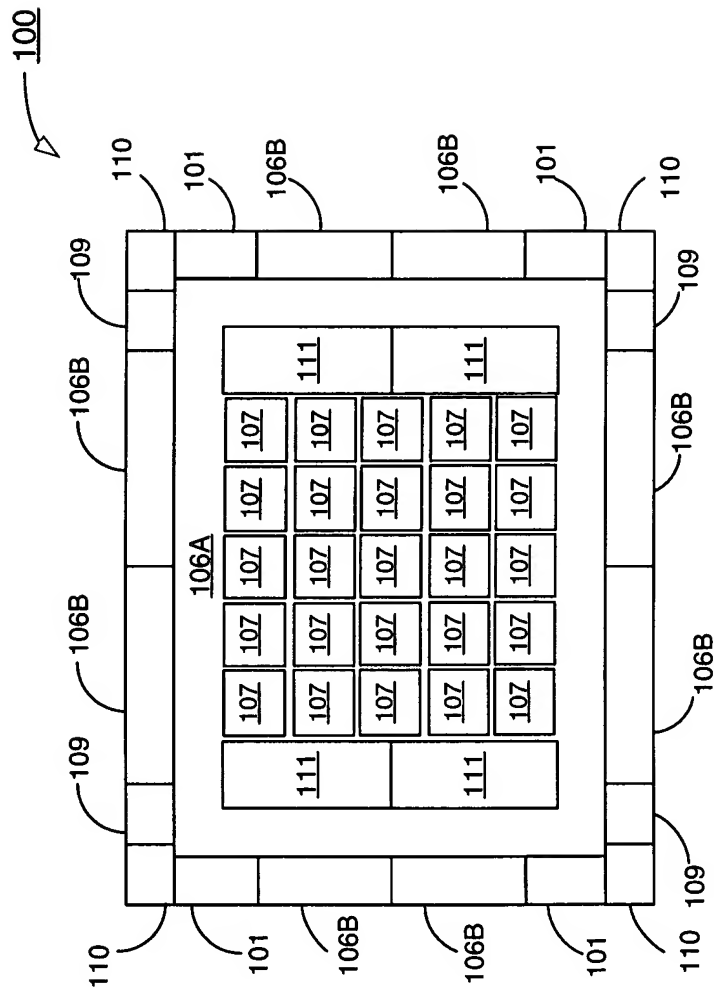
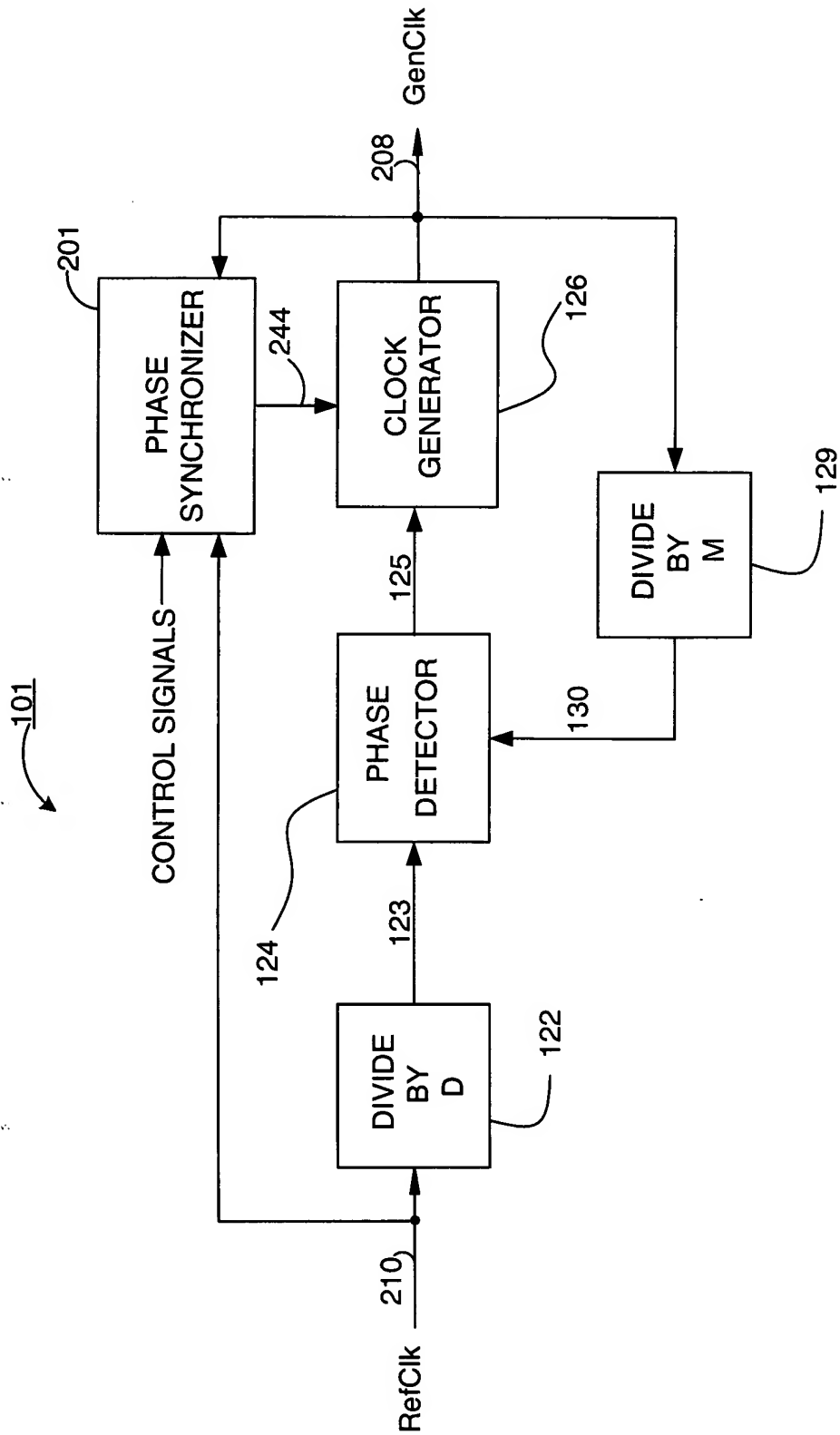
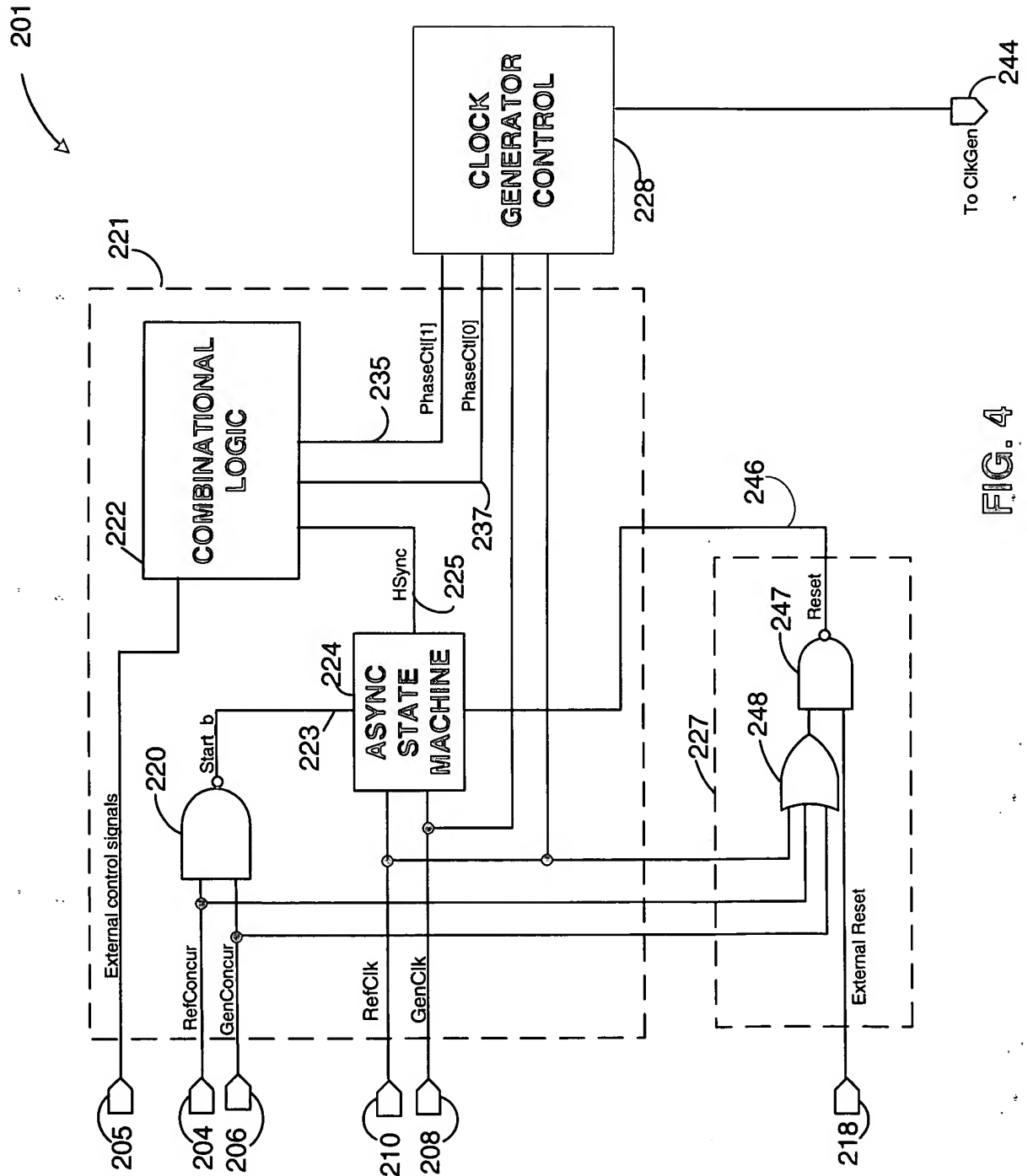


FIG. 2

**FIG. 3**



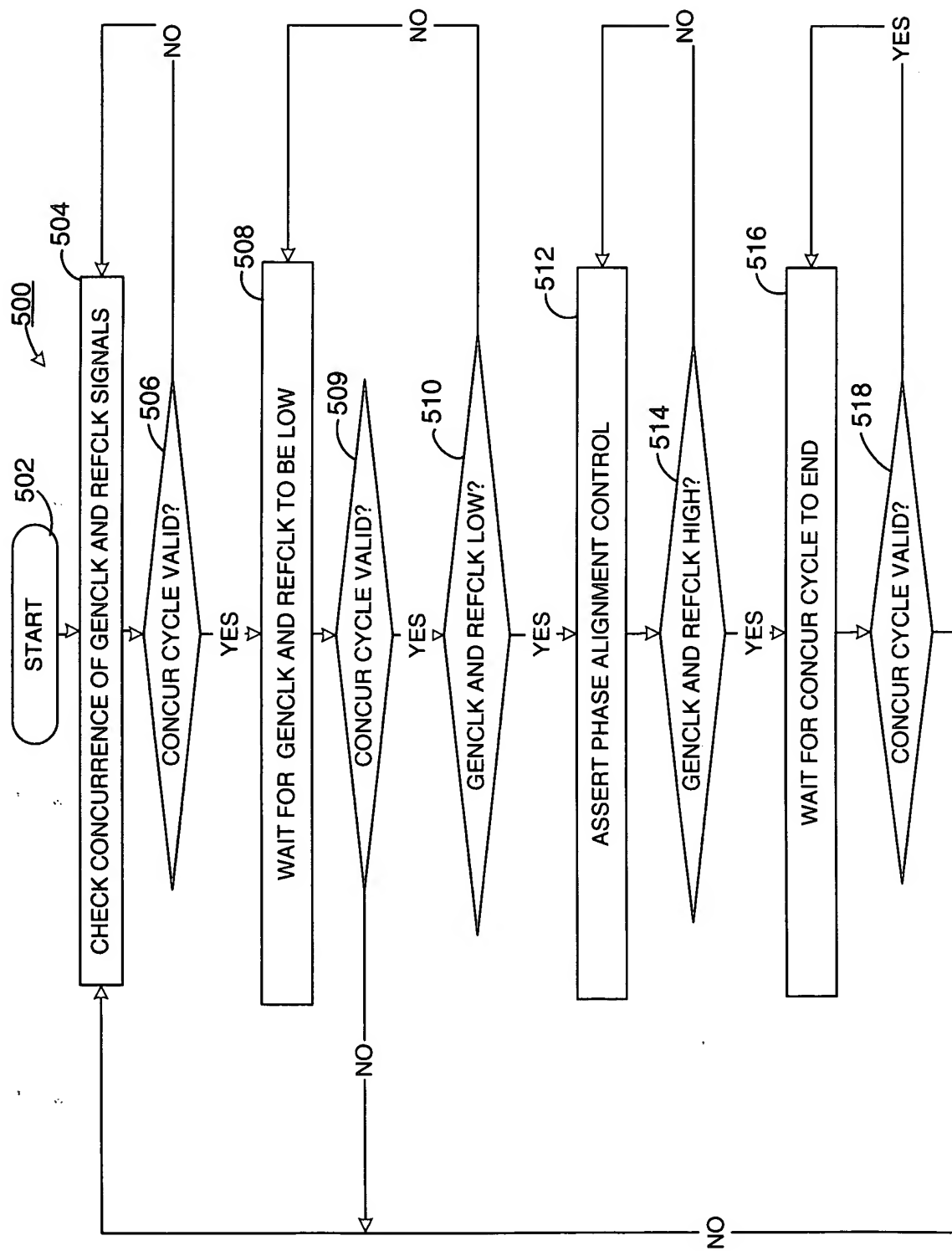


FIG. 5

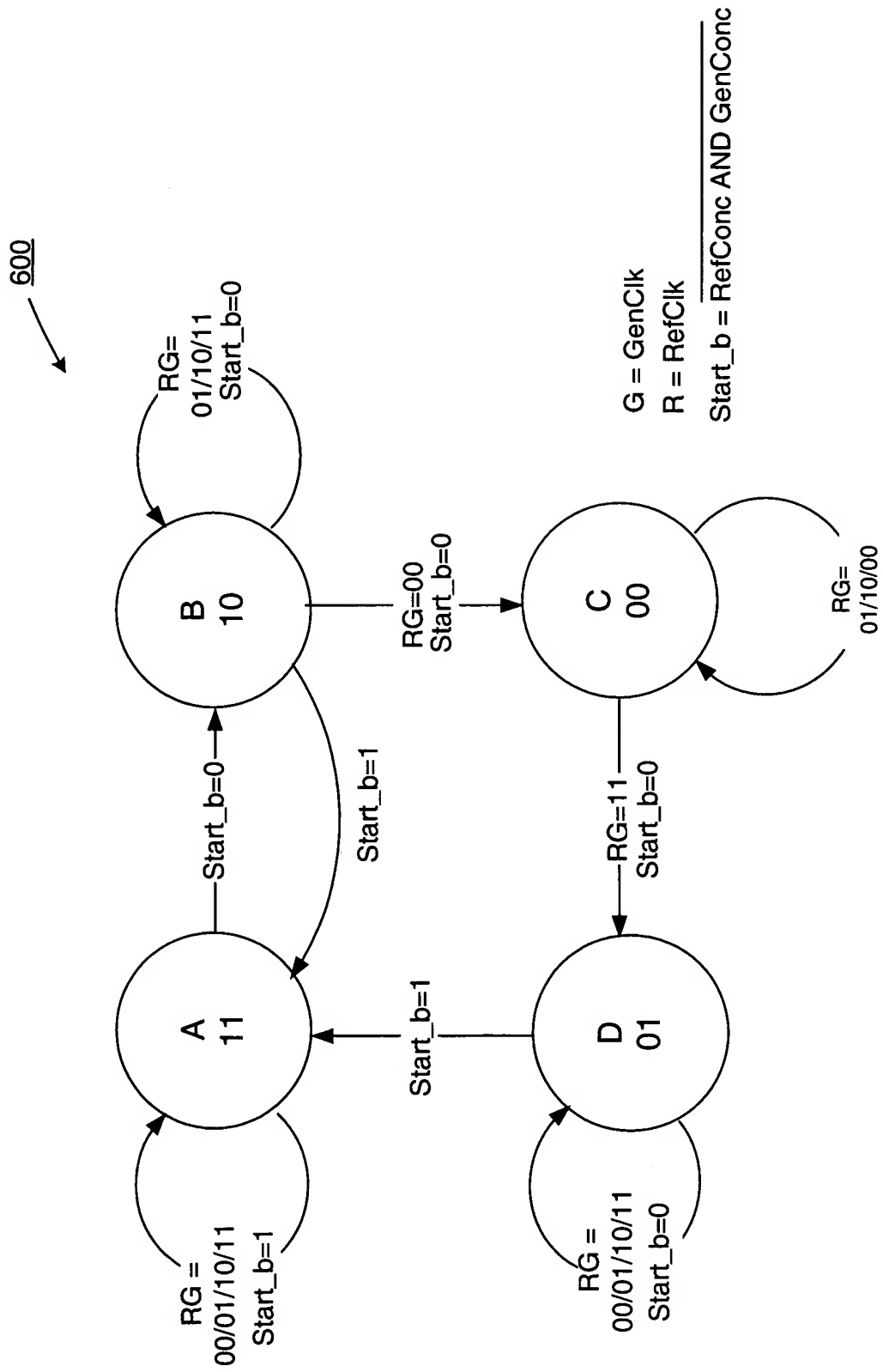


FIG. 6

700

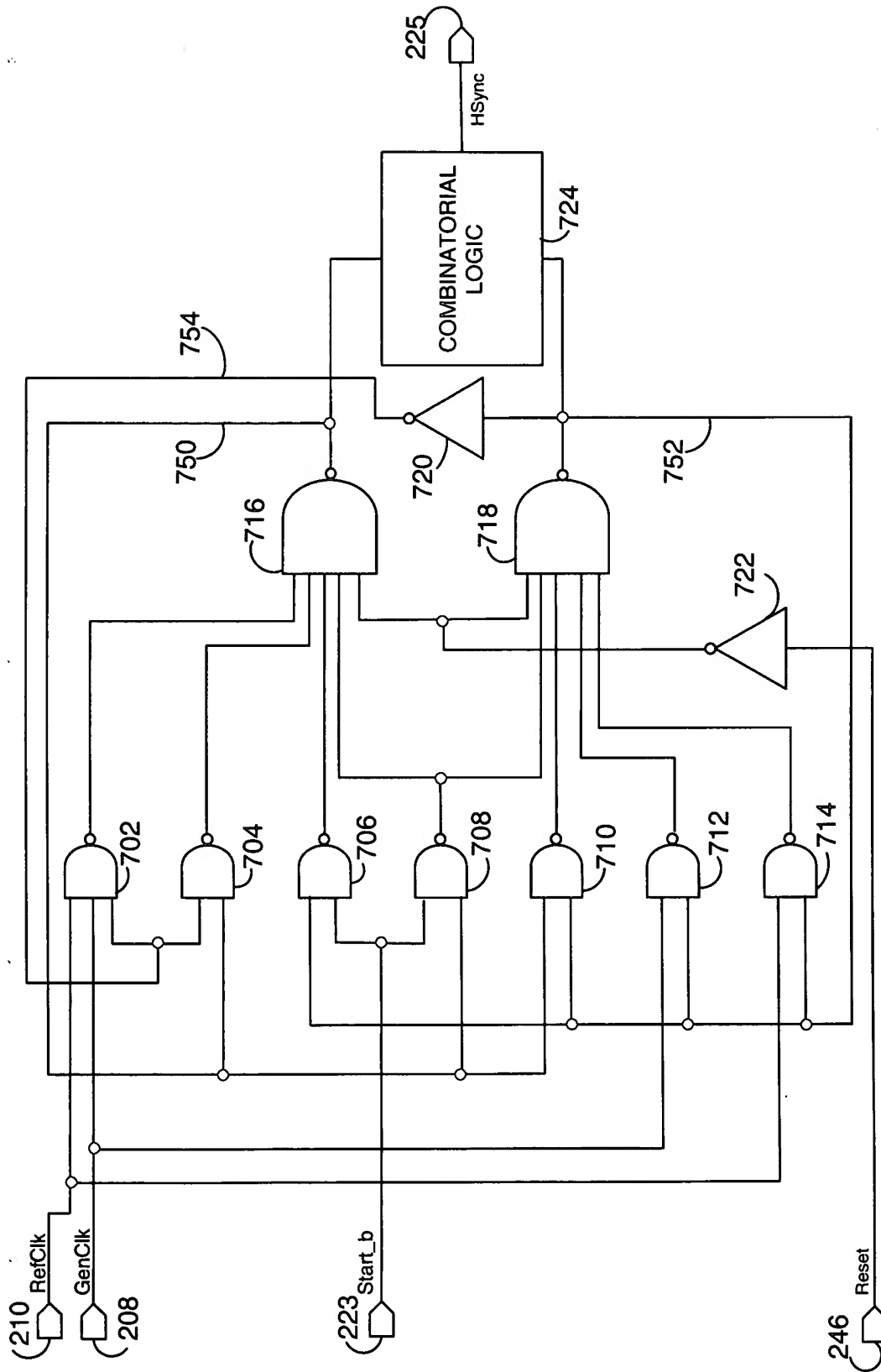


FIG. 7

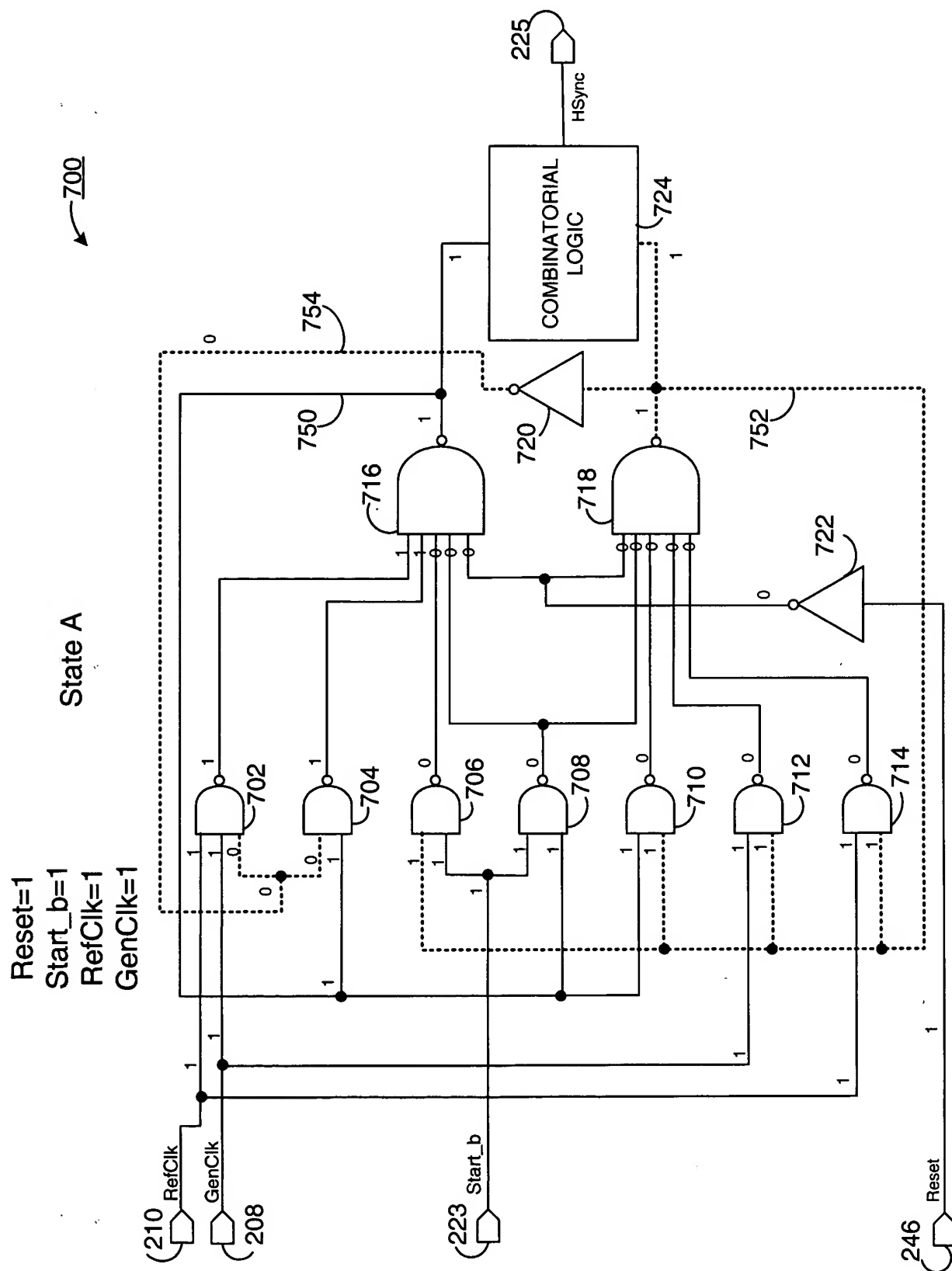


FIG. 7A



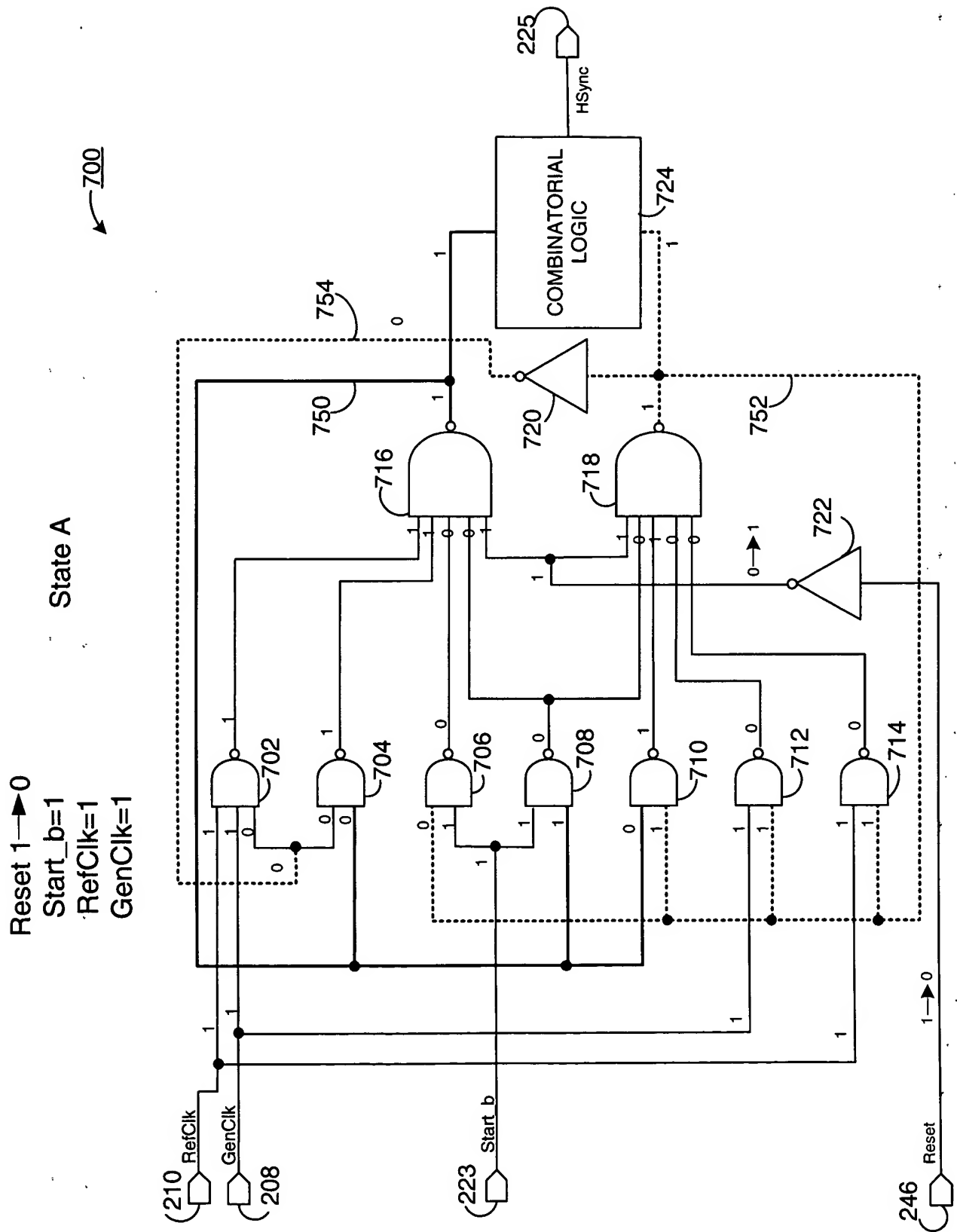
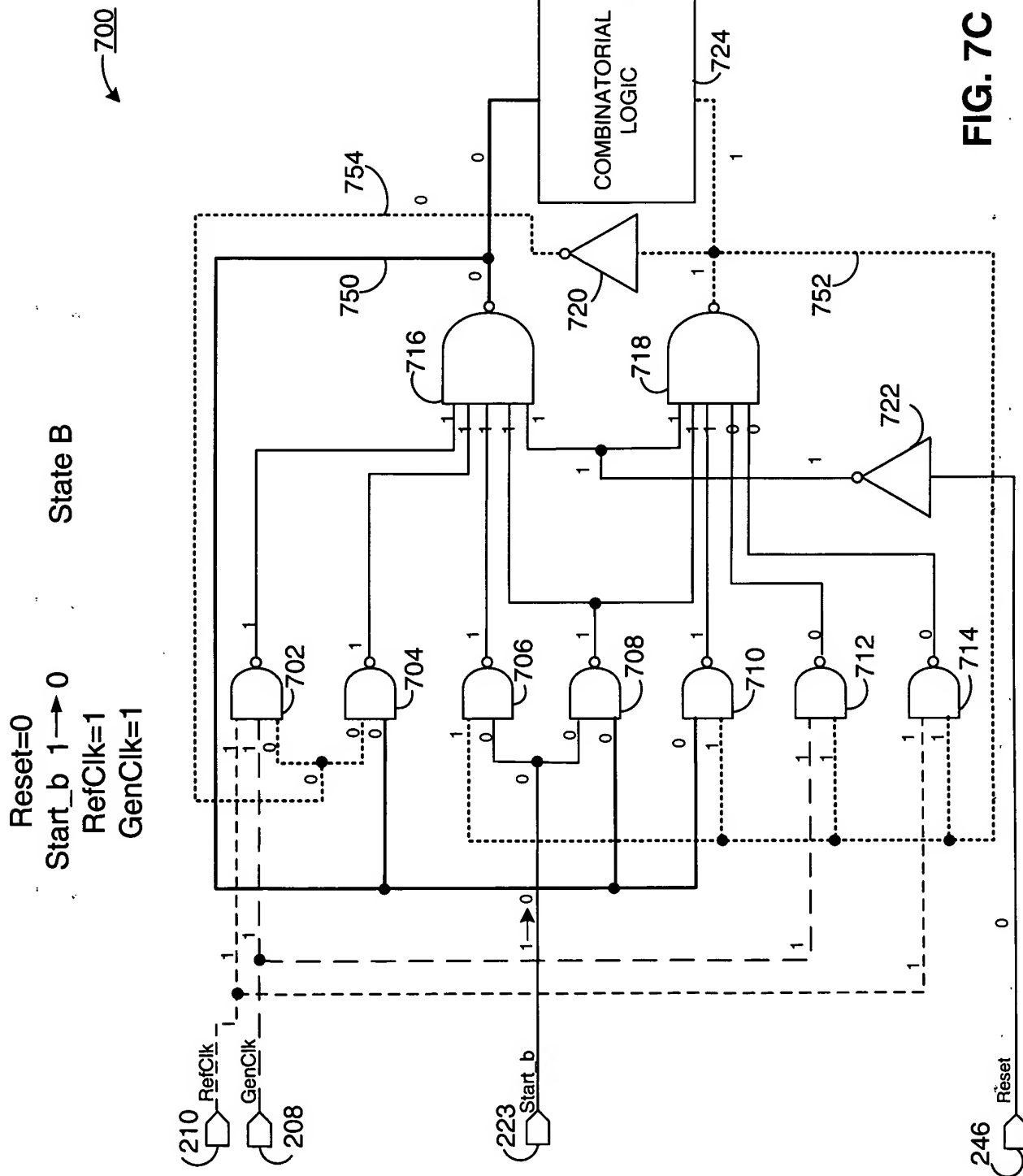


FIG. 7B



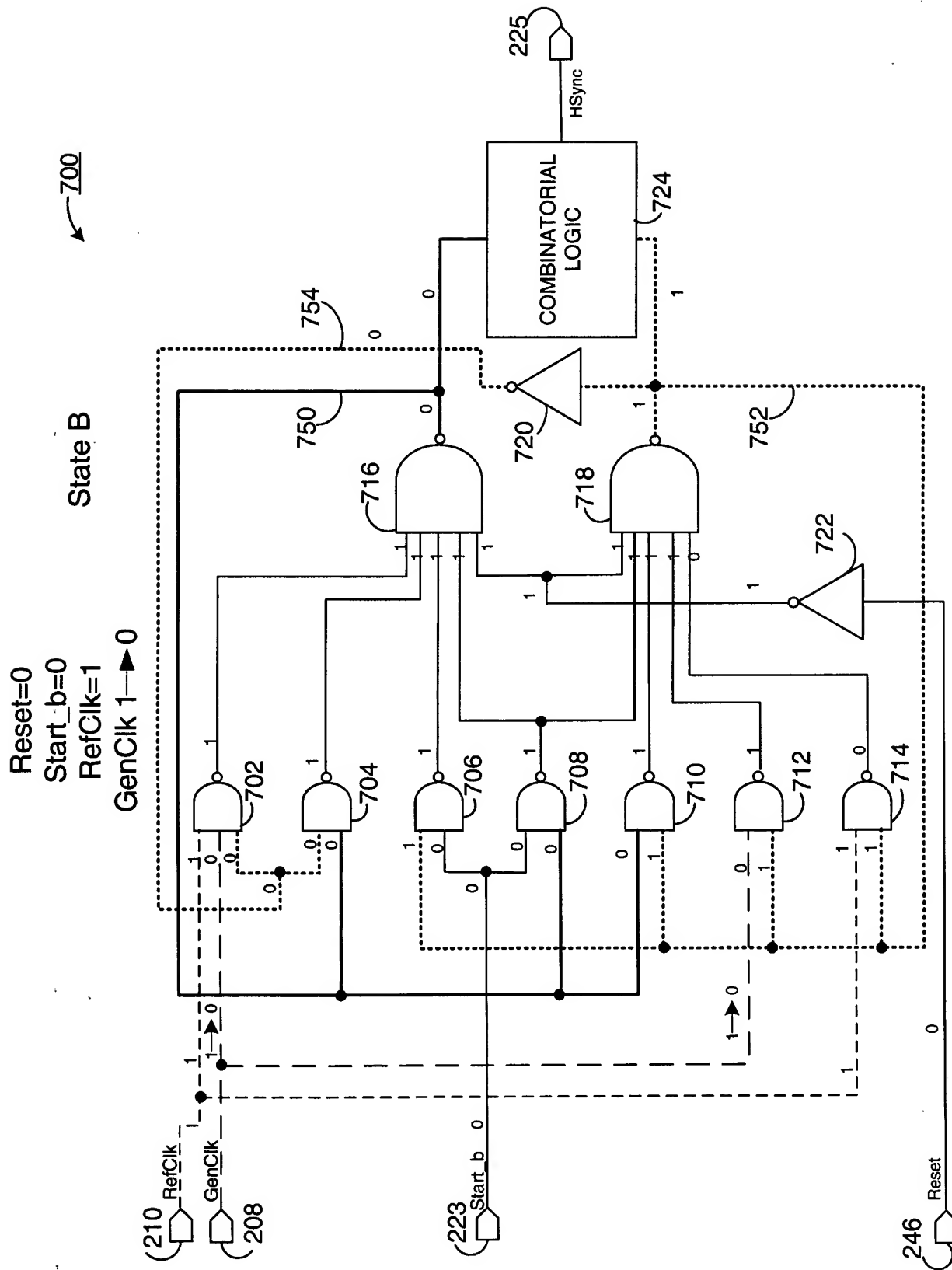
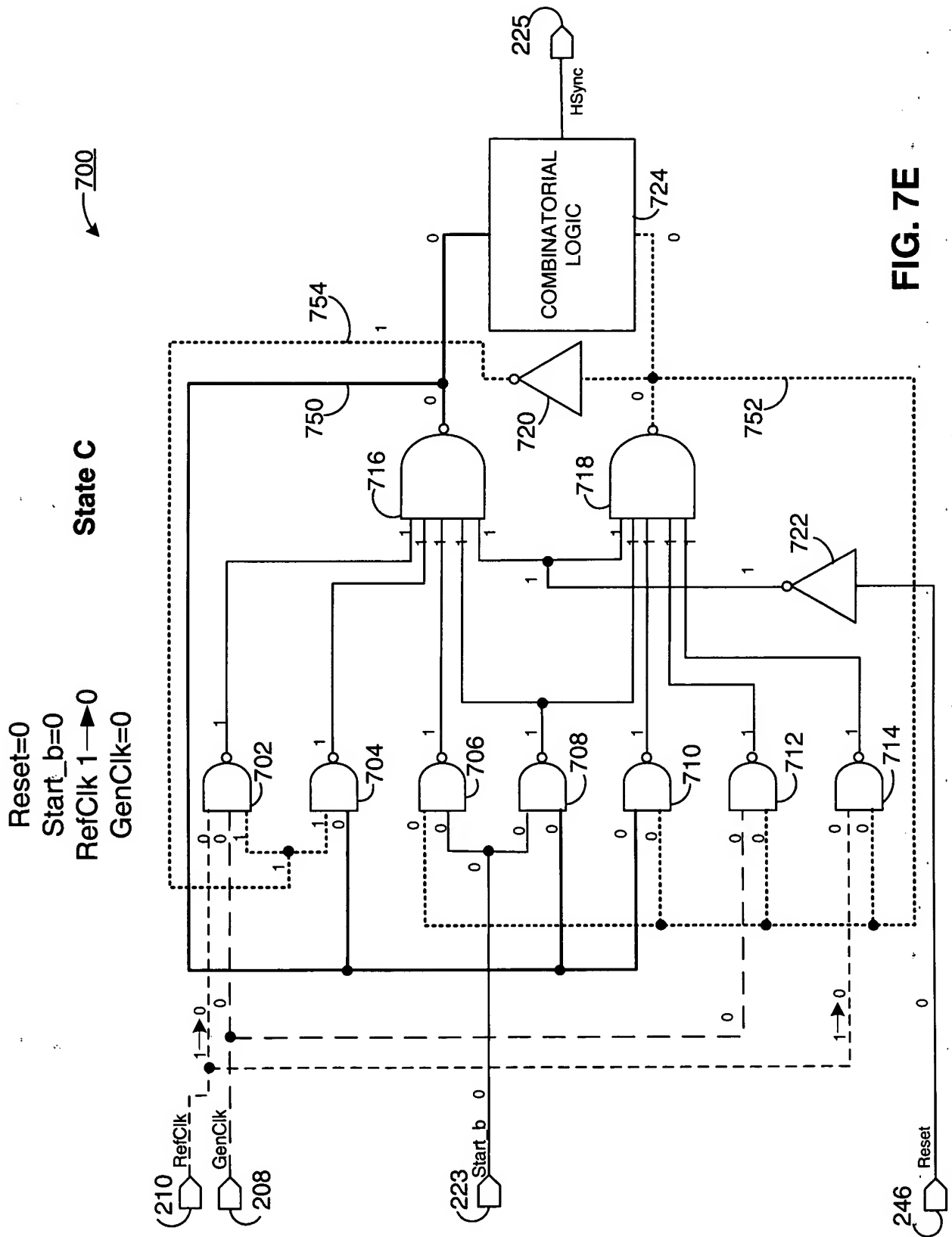


FIG. 7D



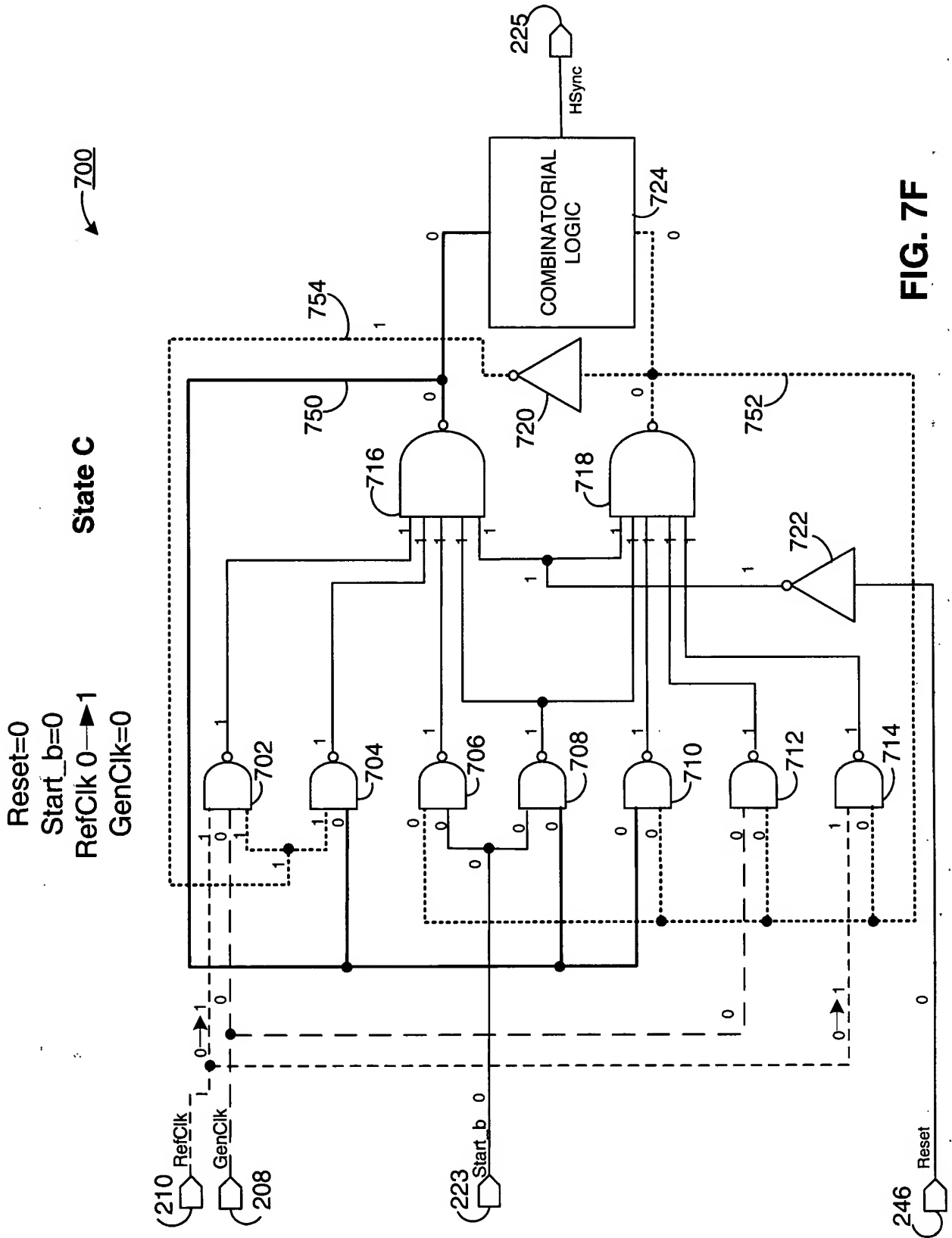
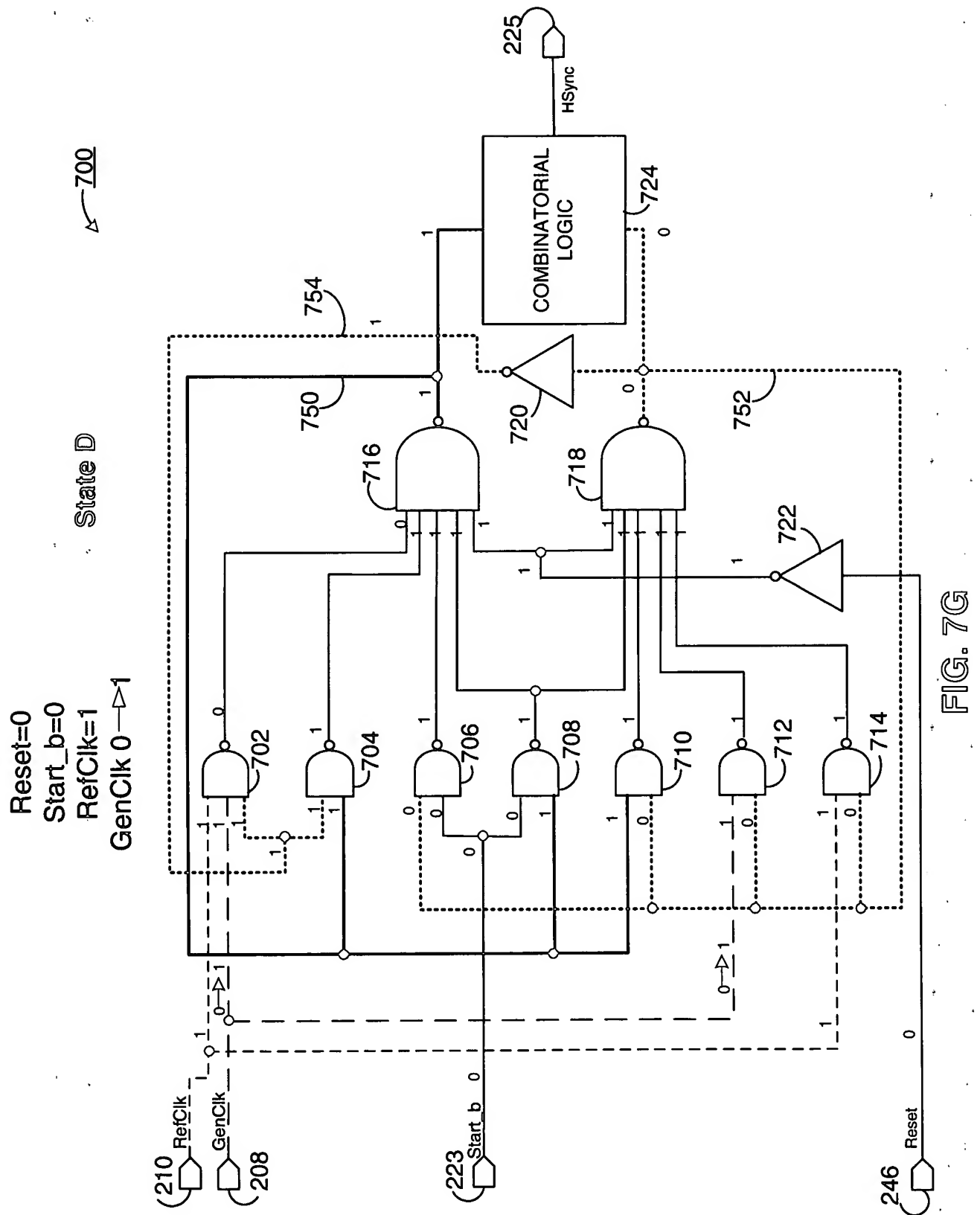


FIG. 7F



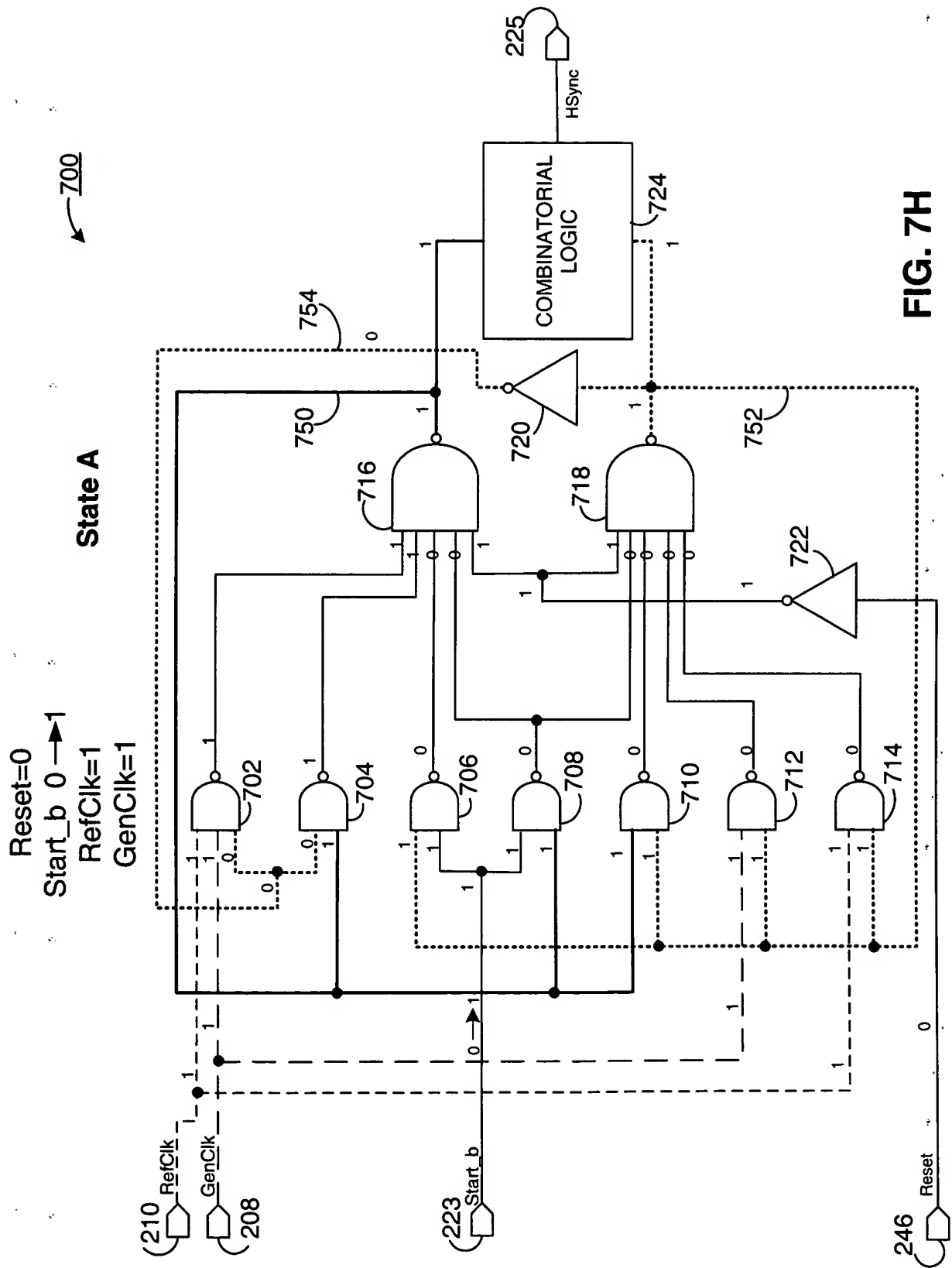


FIG. 7H

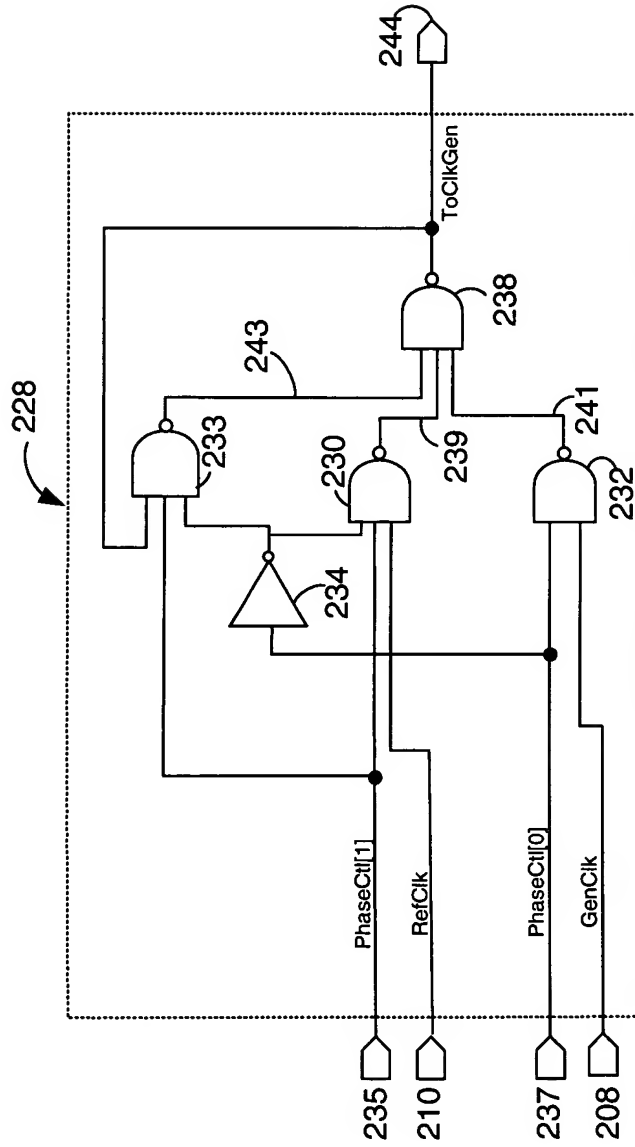


FIG. 8



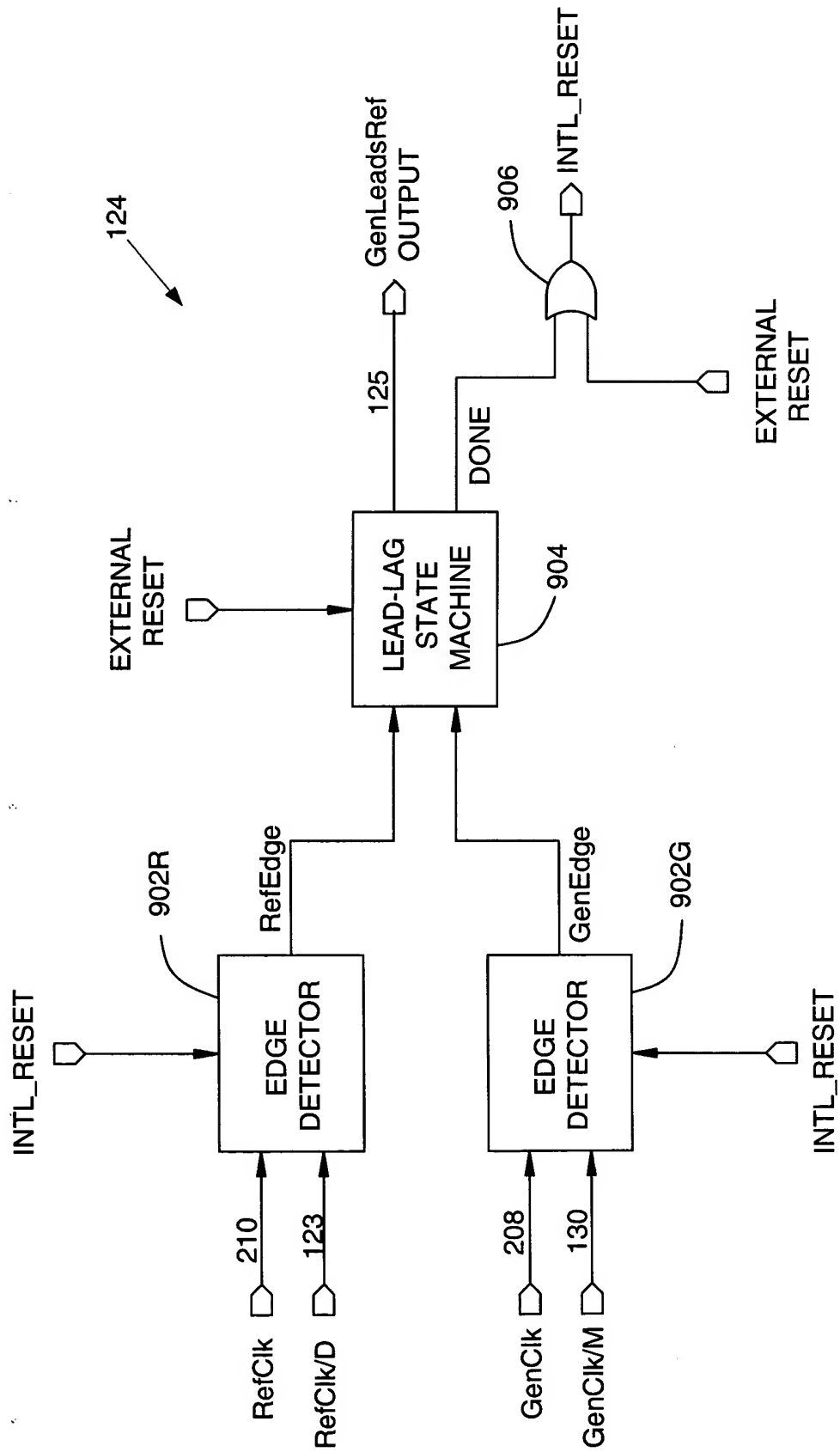
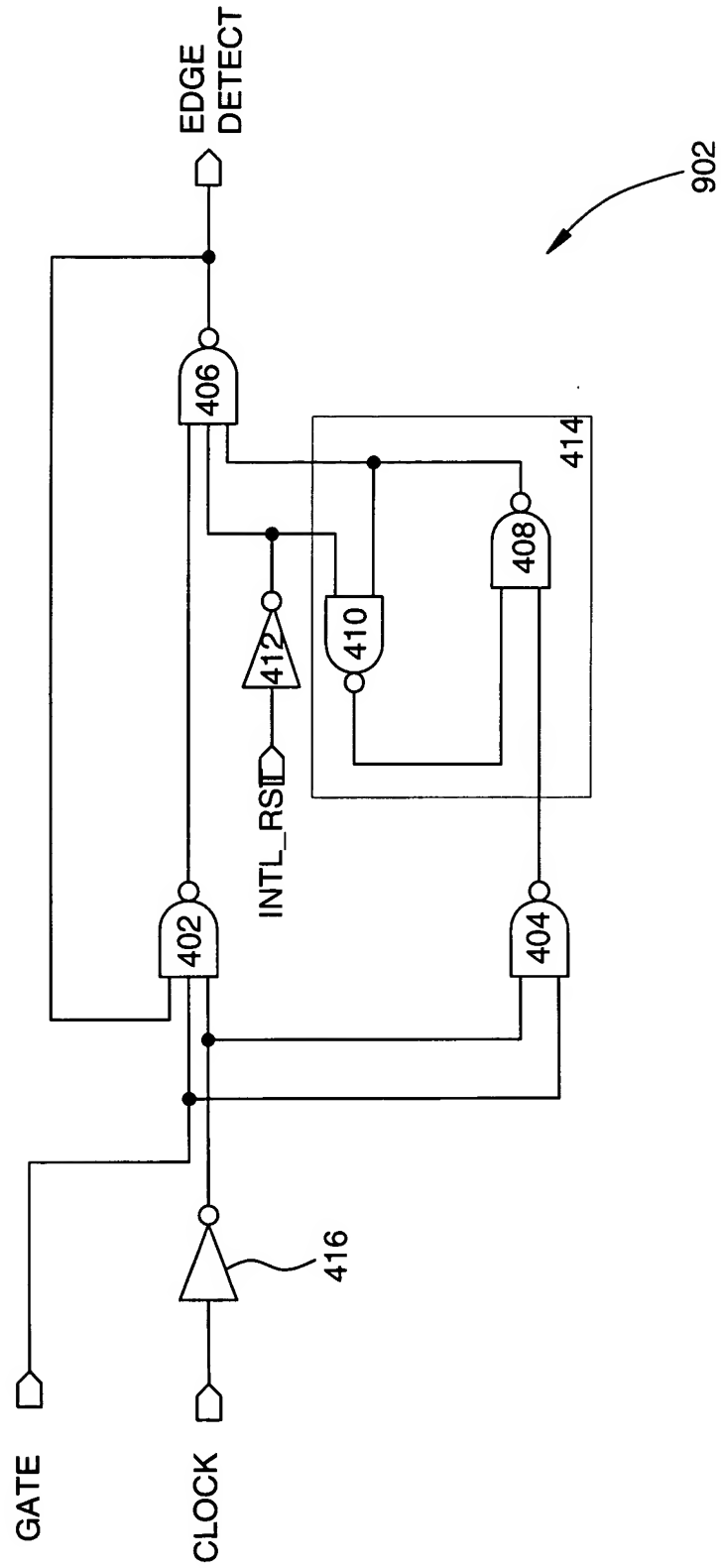


FIG. 9



**FIG. 10**

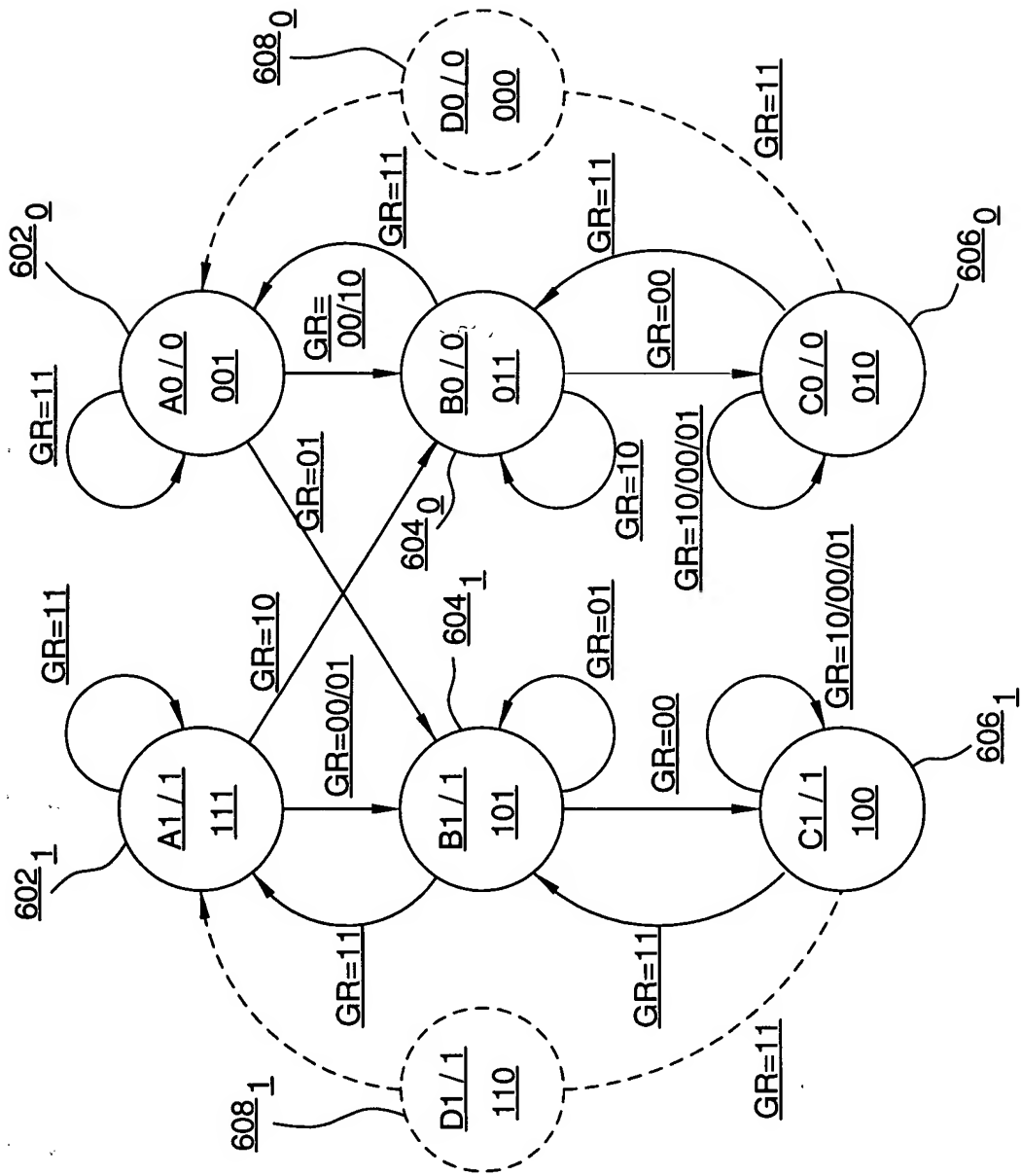
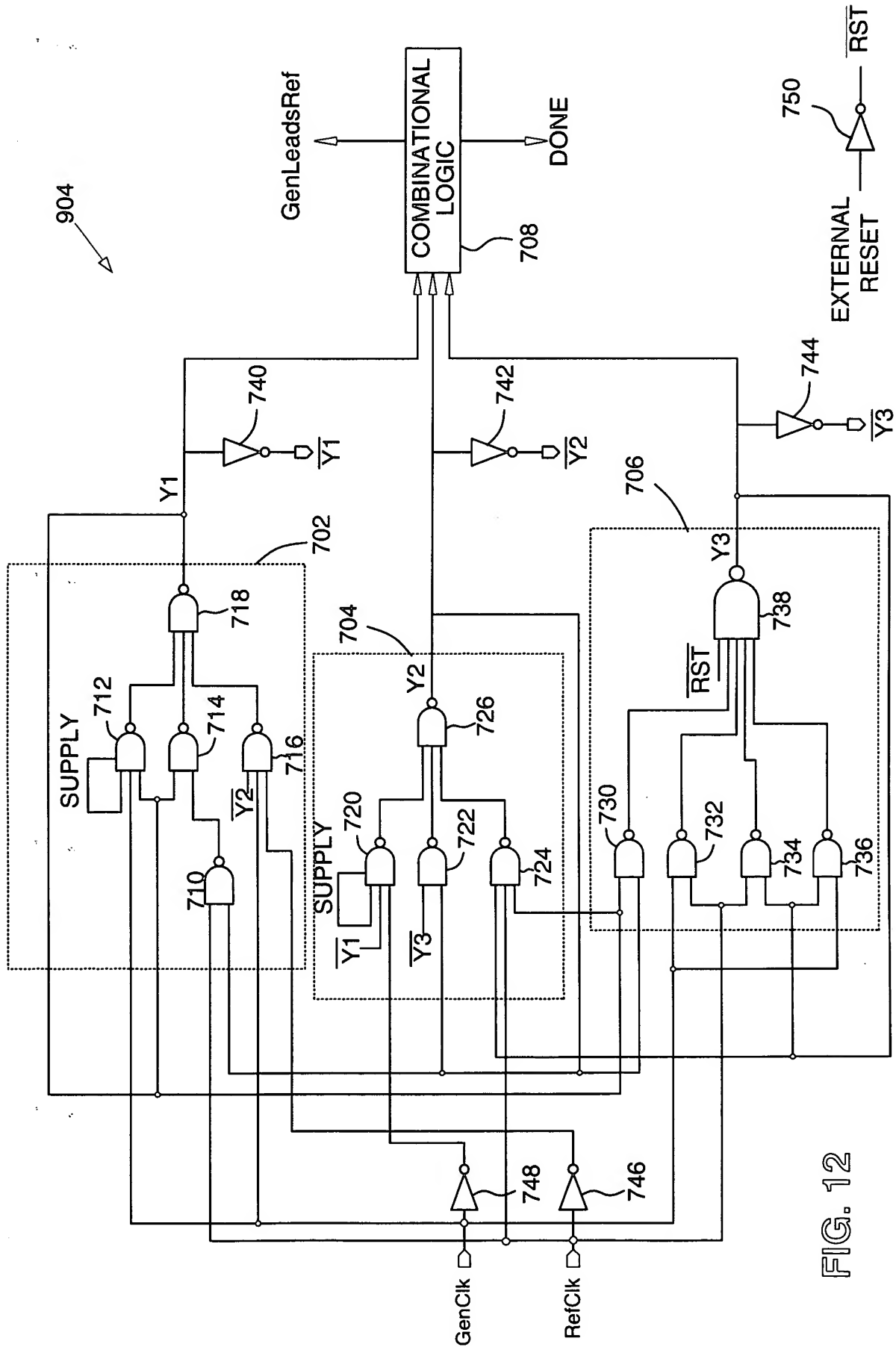
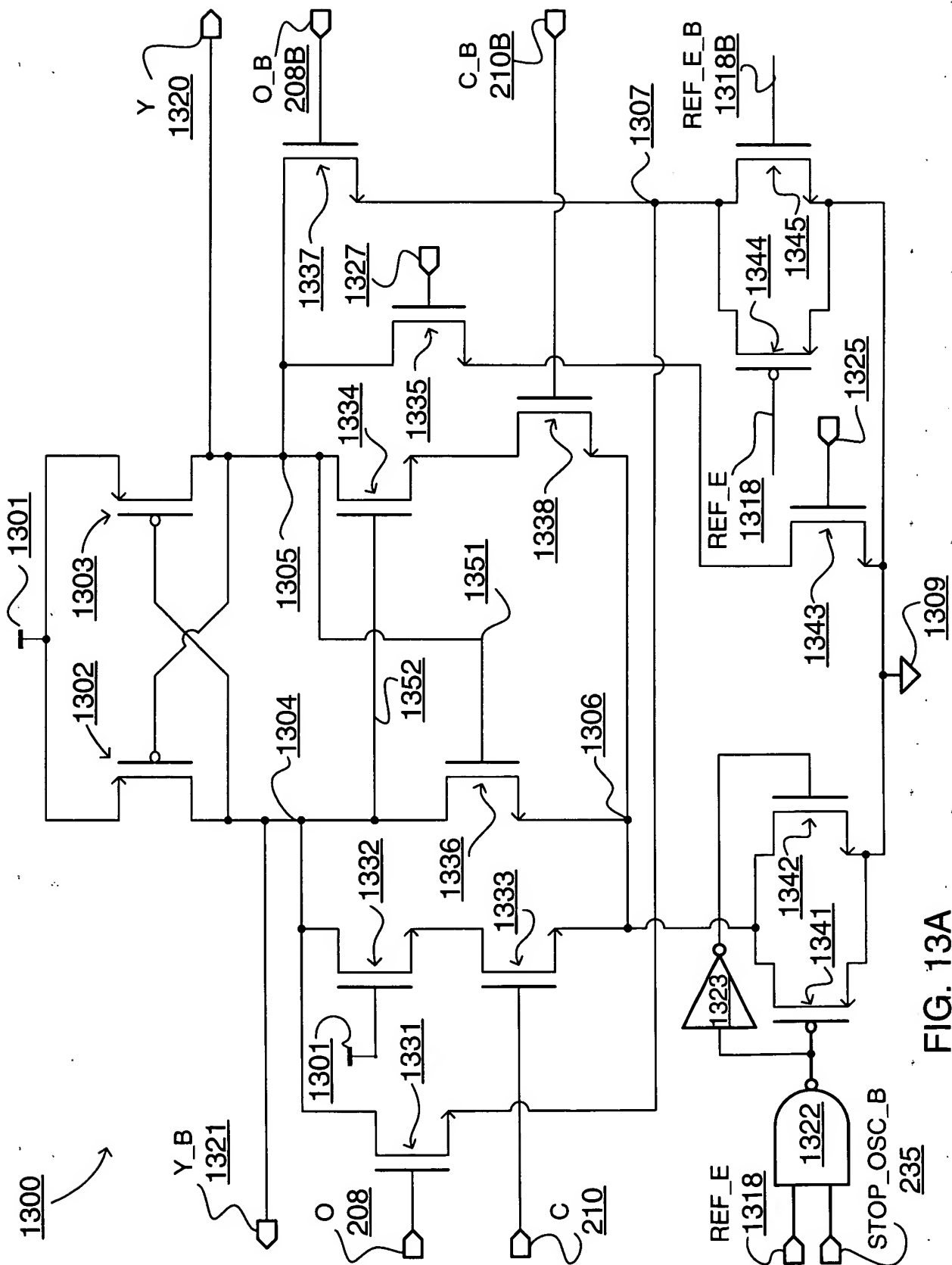


FIG. 11





1310

STATES 1314	STOP_OSC 1317	REF_E 1318	DIFF_OUT 1319
BEFORE LOCK 1311	1	X(FORCED 1)	0
OSCILLATOR ALIGNMENT 1312	0	0	OSC.SIGNAL
HARD-PHASE ALIGNMENT 1313	0	1	REF.EDGE

1315

1316

FIG. 13B

1350

1351

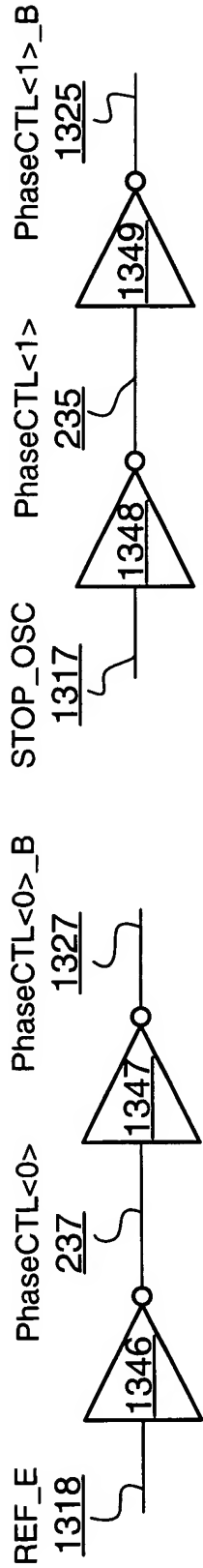


FIG. 13C

FIG. 13D

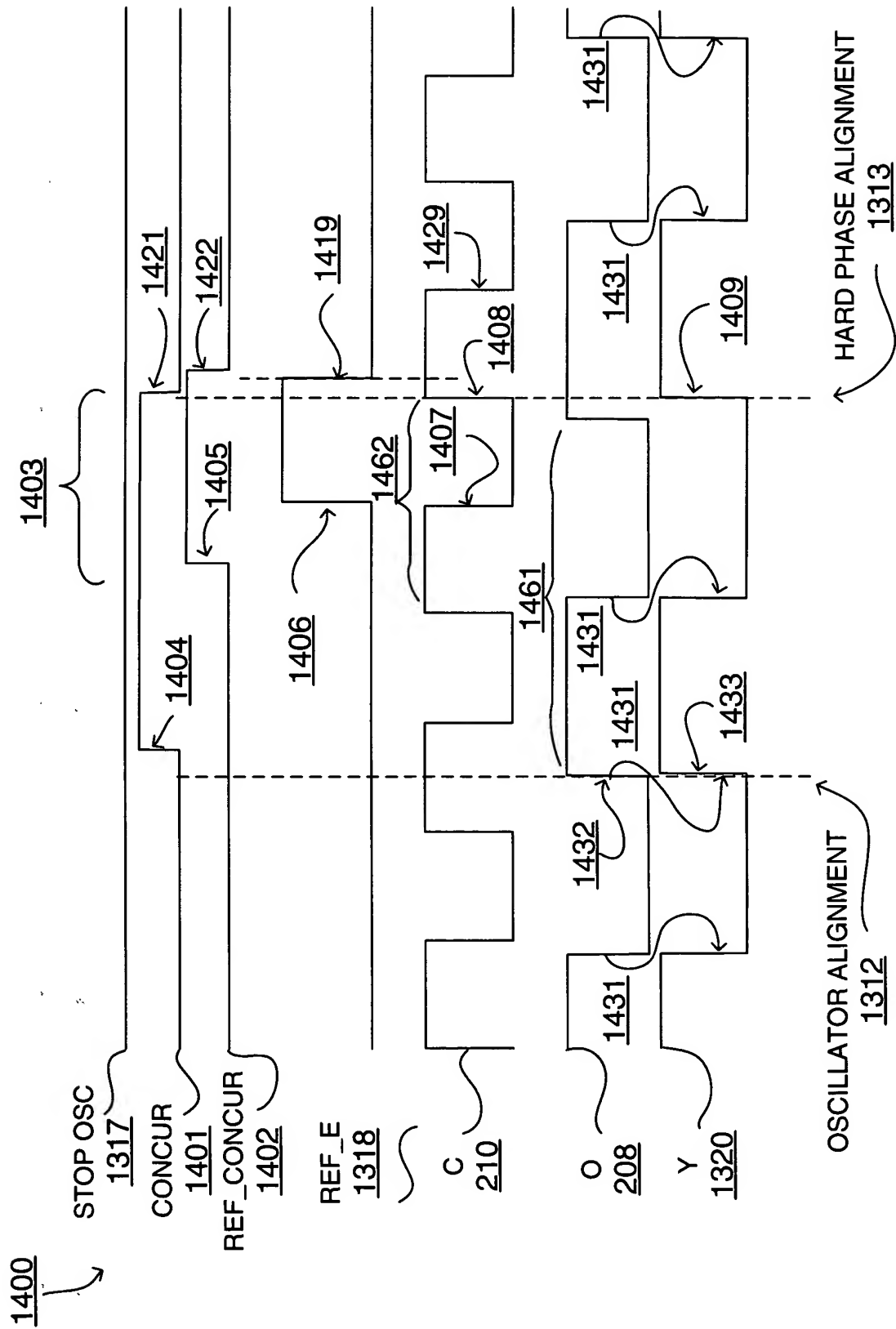
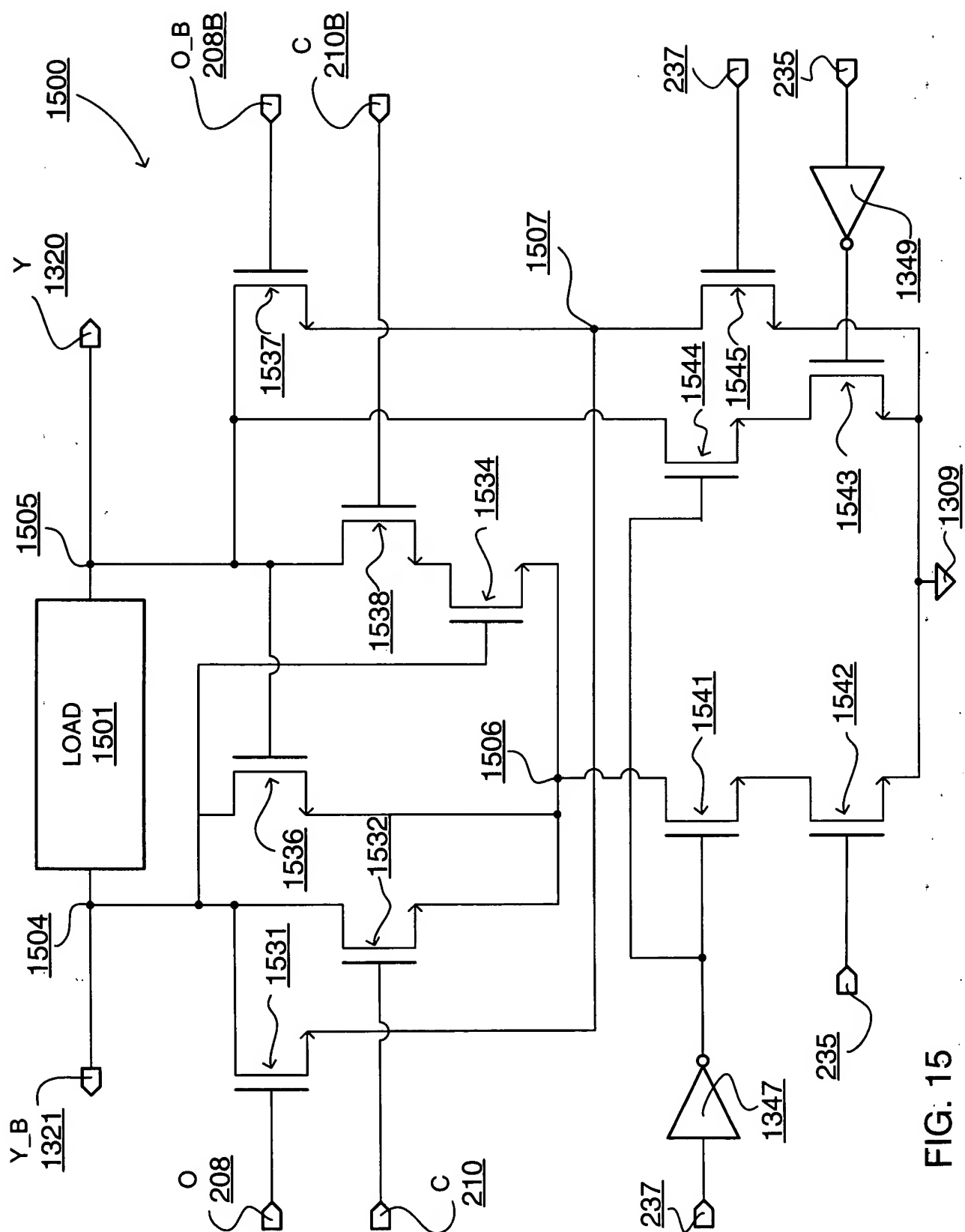


FIG. 14



**FIG. 15**



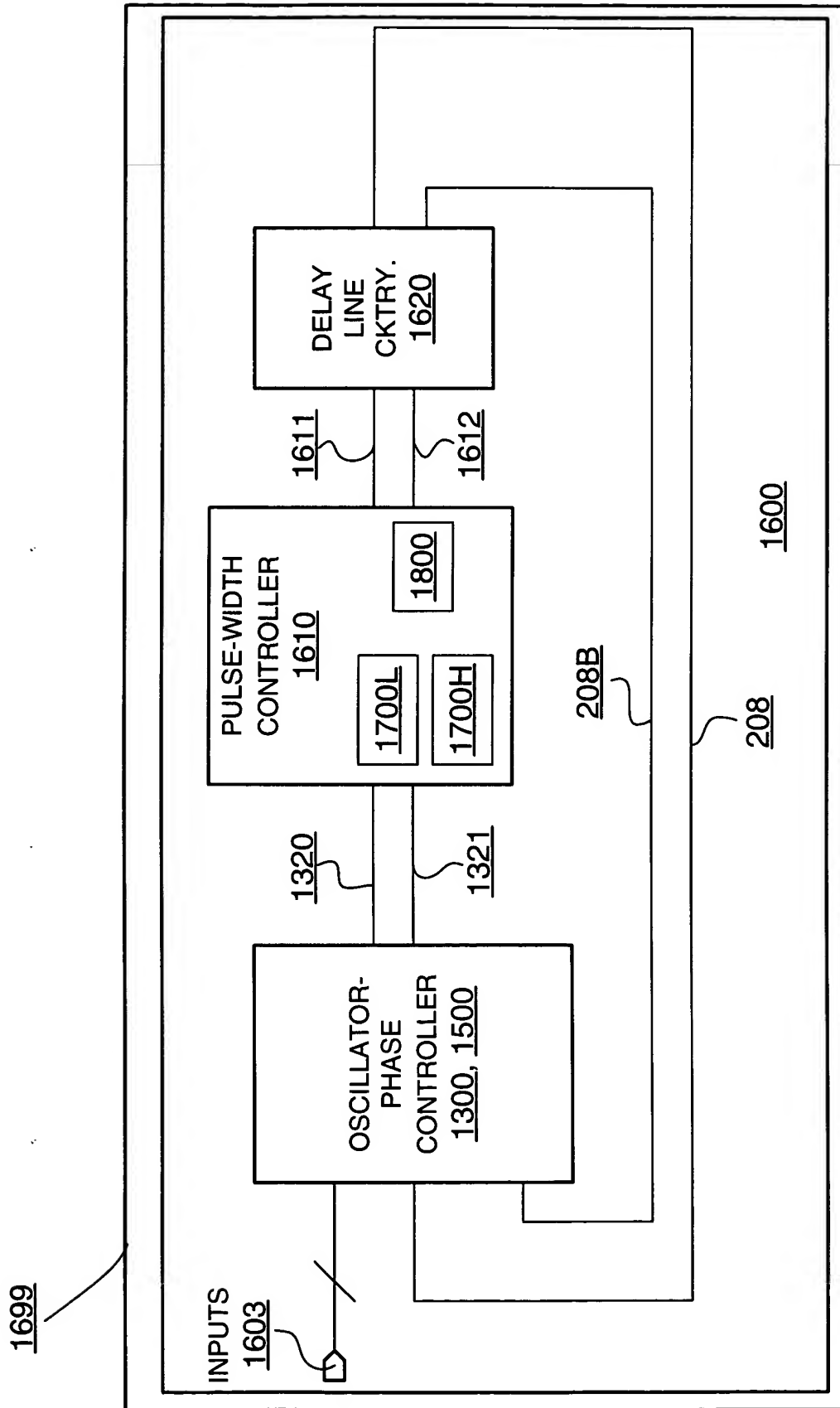


FIG. 16

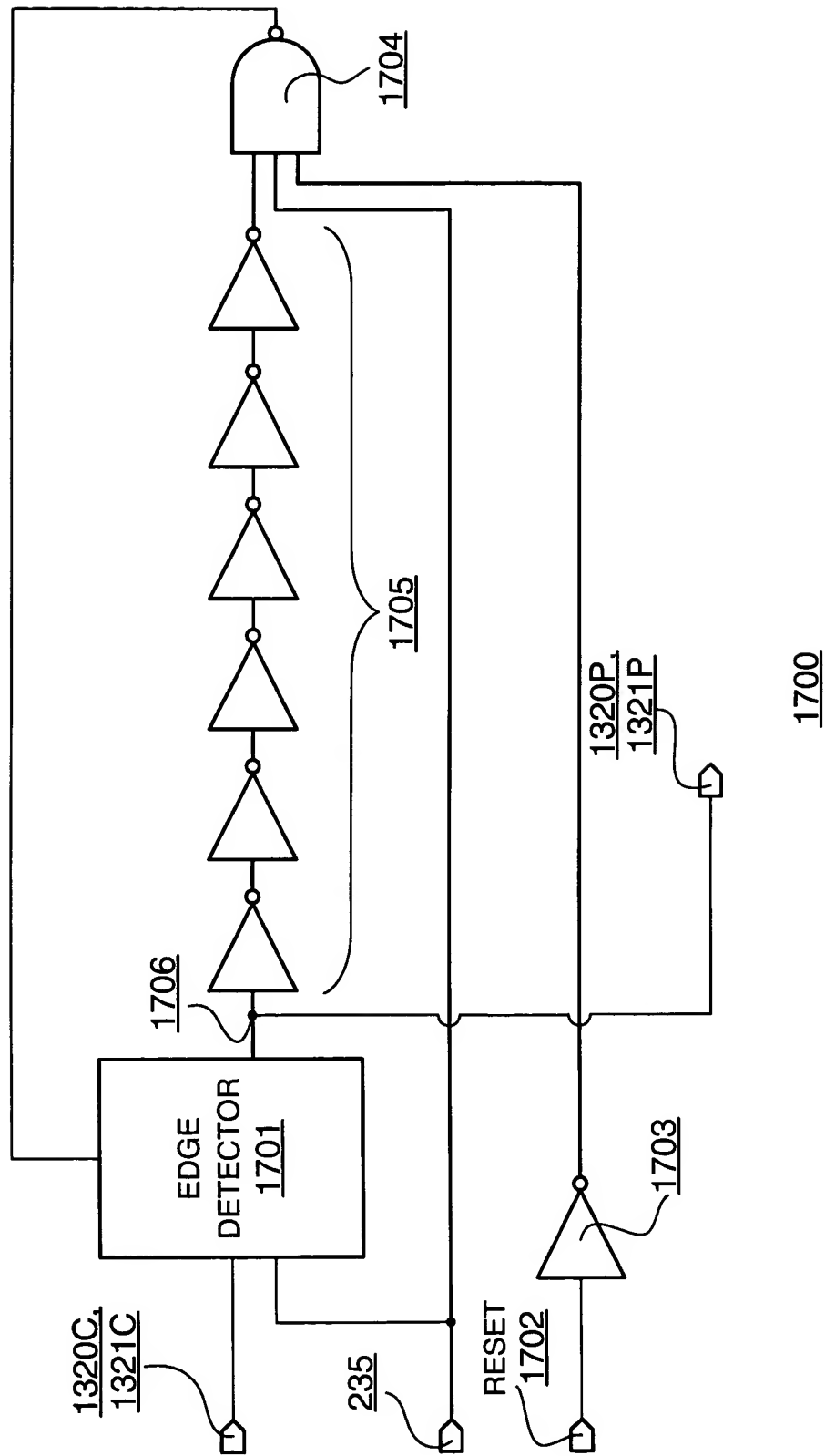


FIG. 17

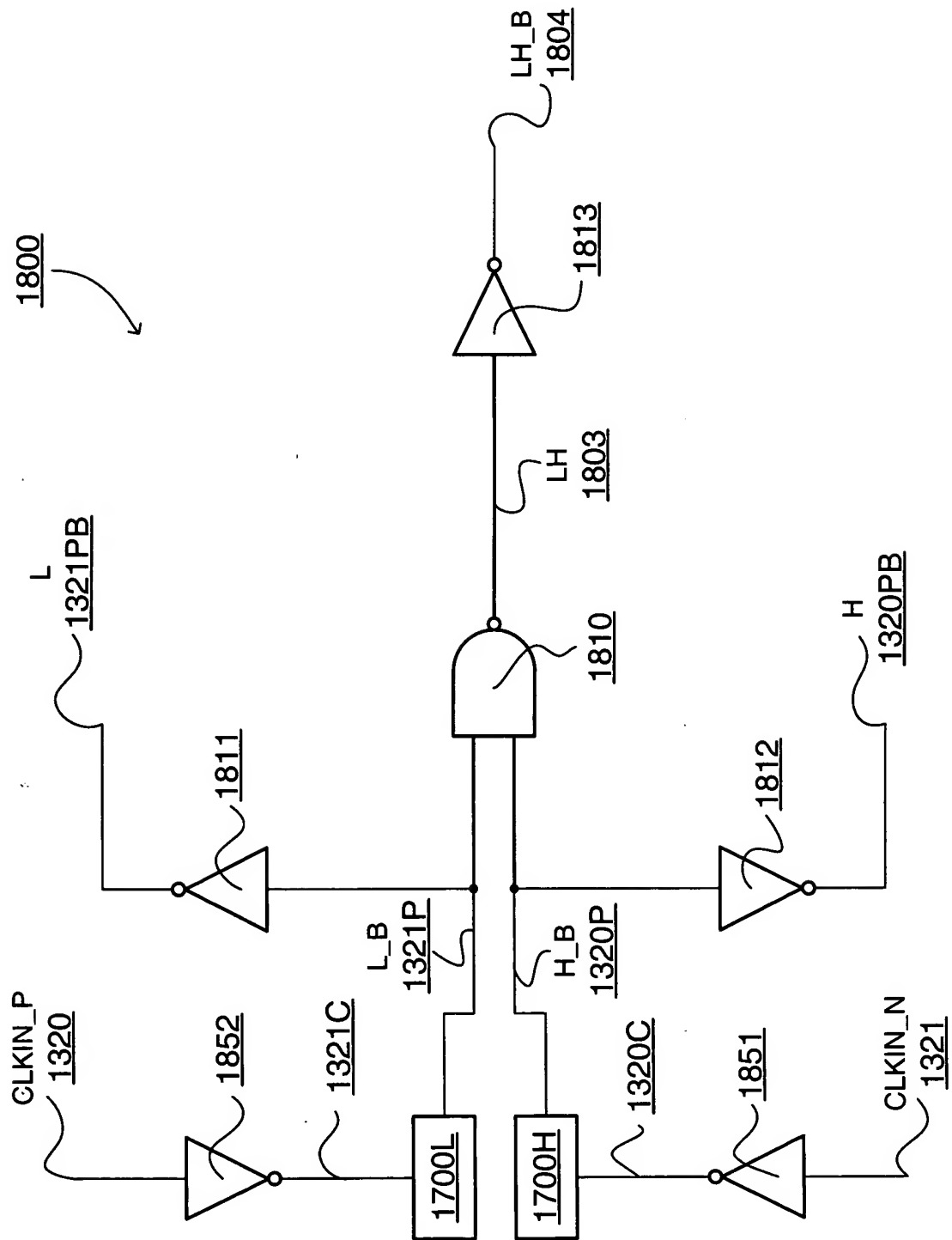


FIG. 18A

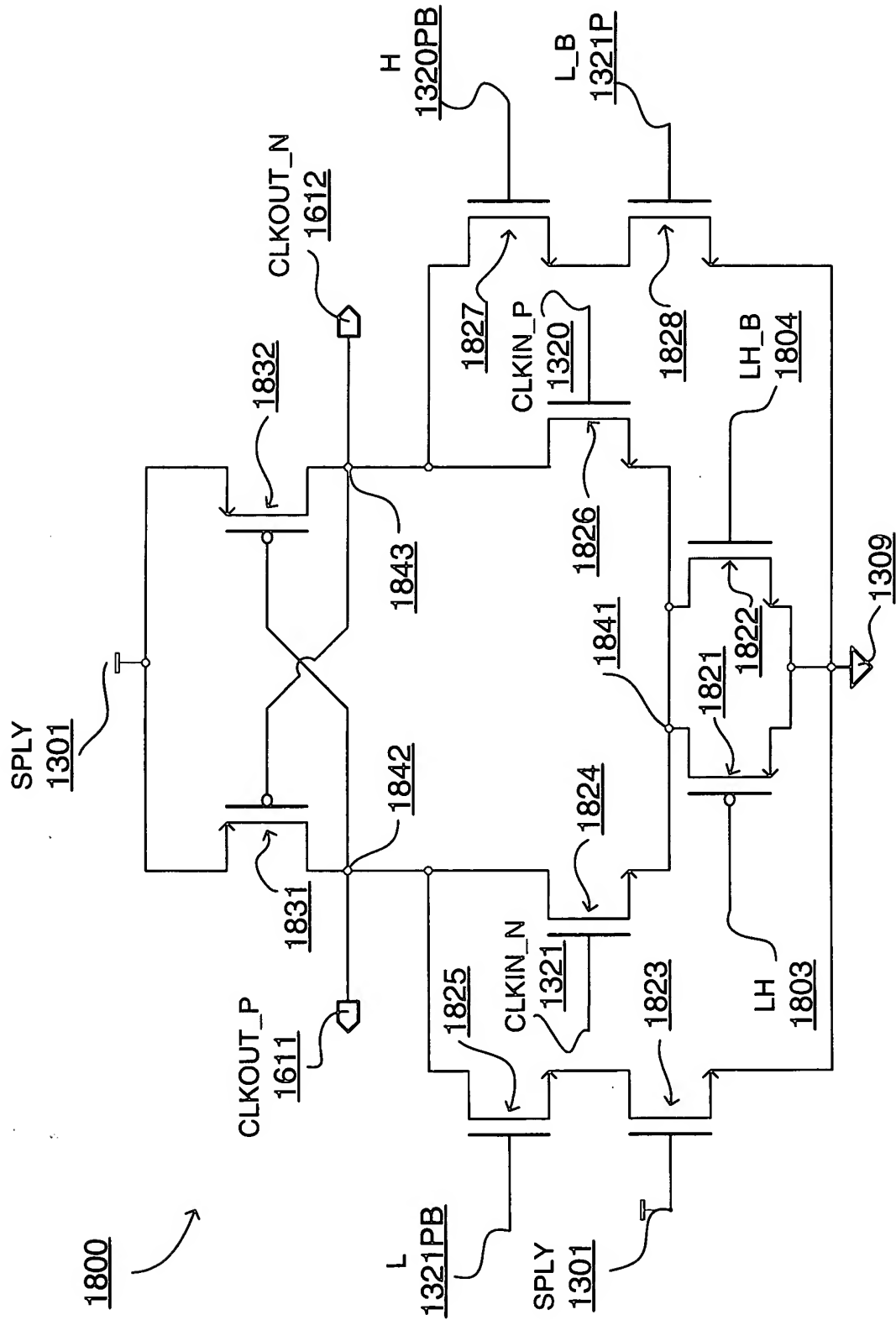


FIG. 18B

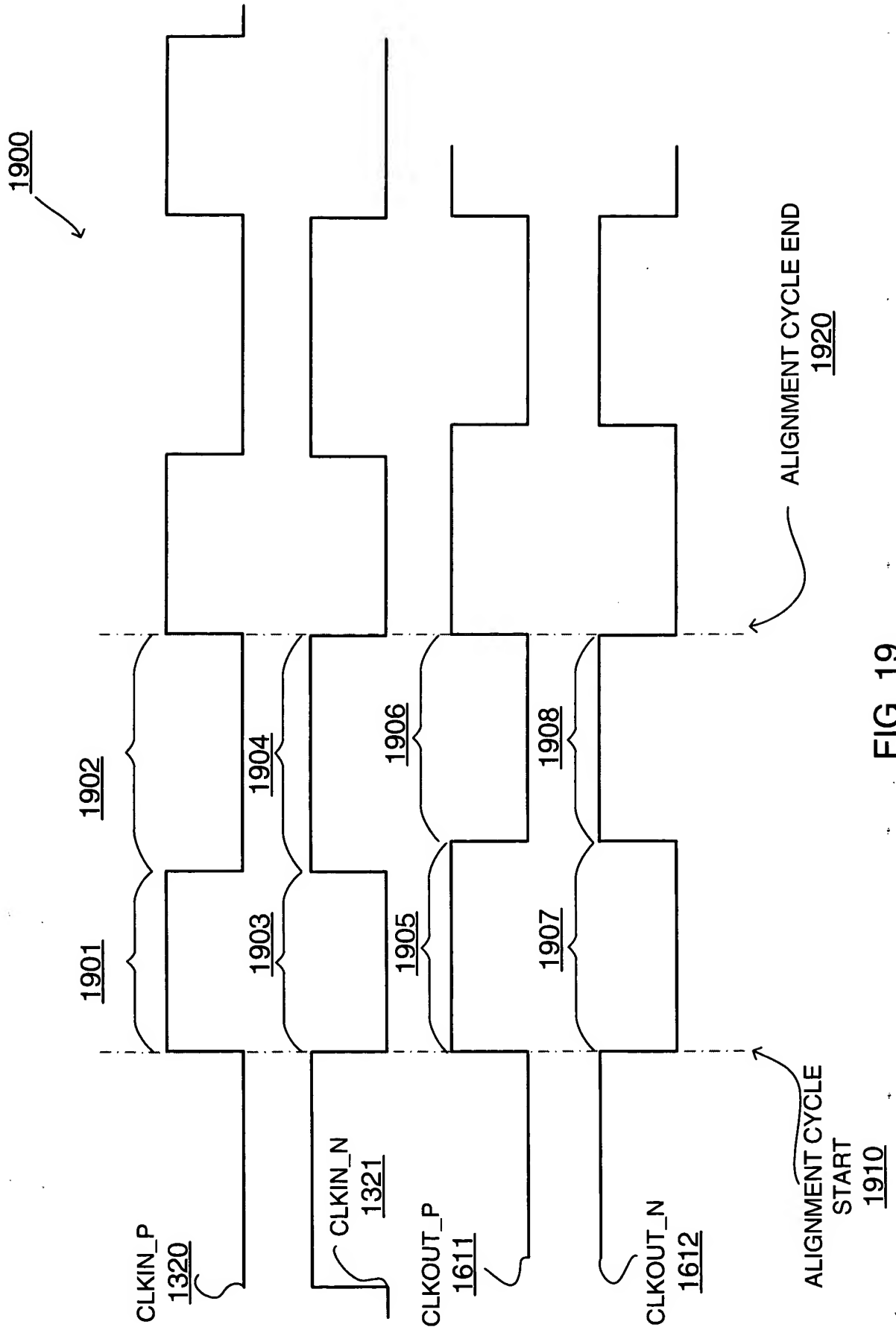


FIG. 19